

An Adaptive Programming Technique for A Re-writable Resistive Memory Device

Christophe Chevallier; Darrell Rinerson

Prepared by Beyer Weaver & Thomas, LLP, (650) 961-8300, Ref: UNTYP022; Contact Morgan Malino

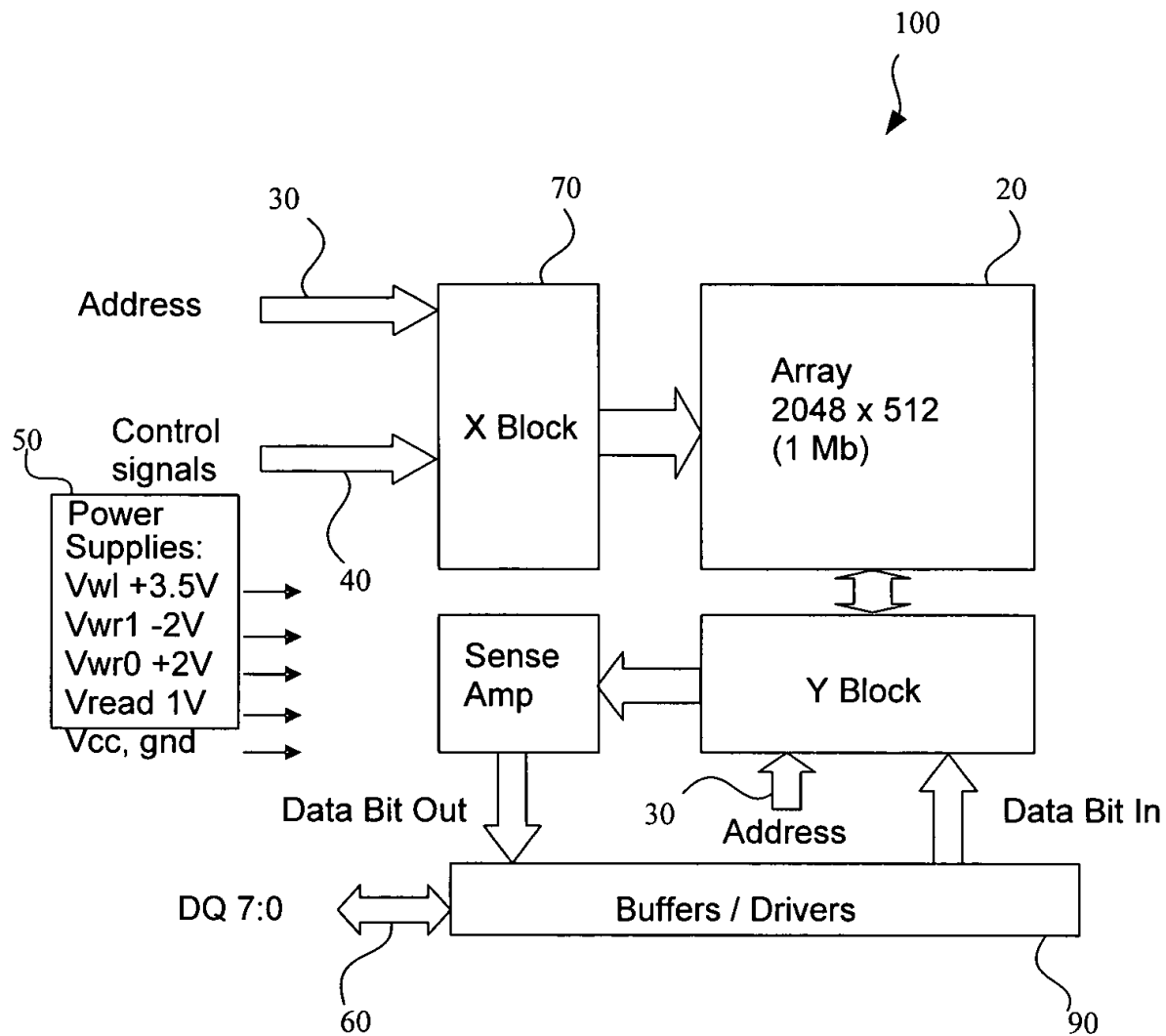


Figure 1

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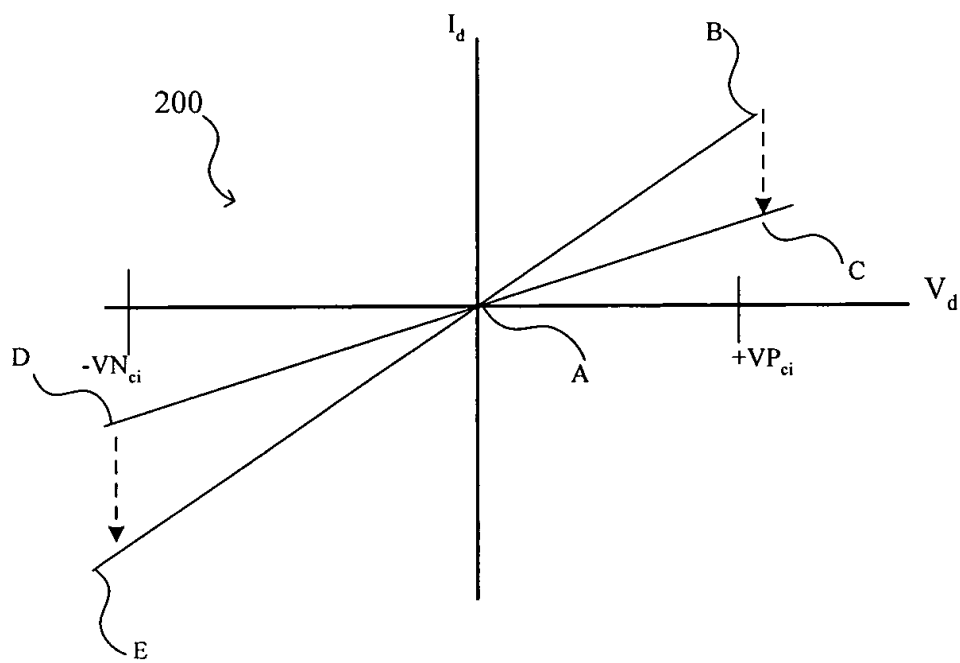


Figure 2

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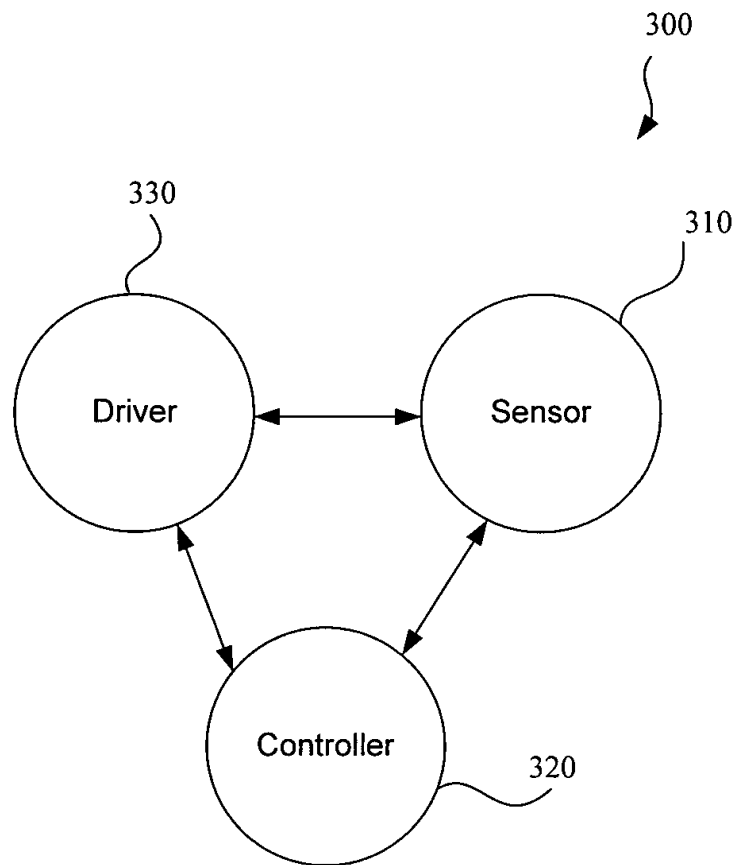


Figure 3

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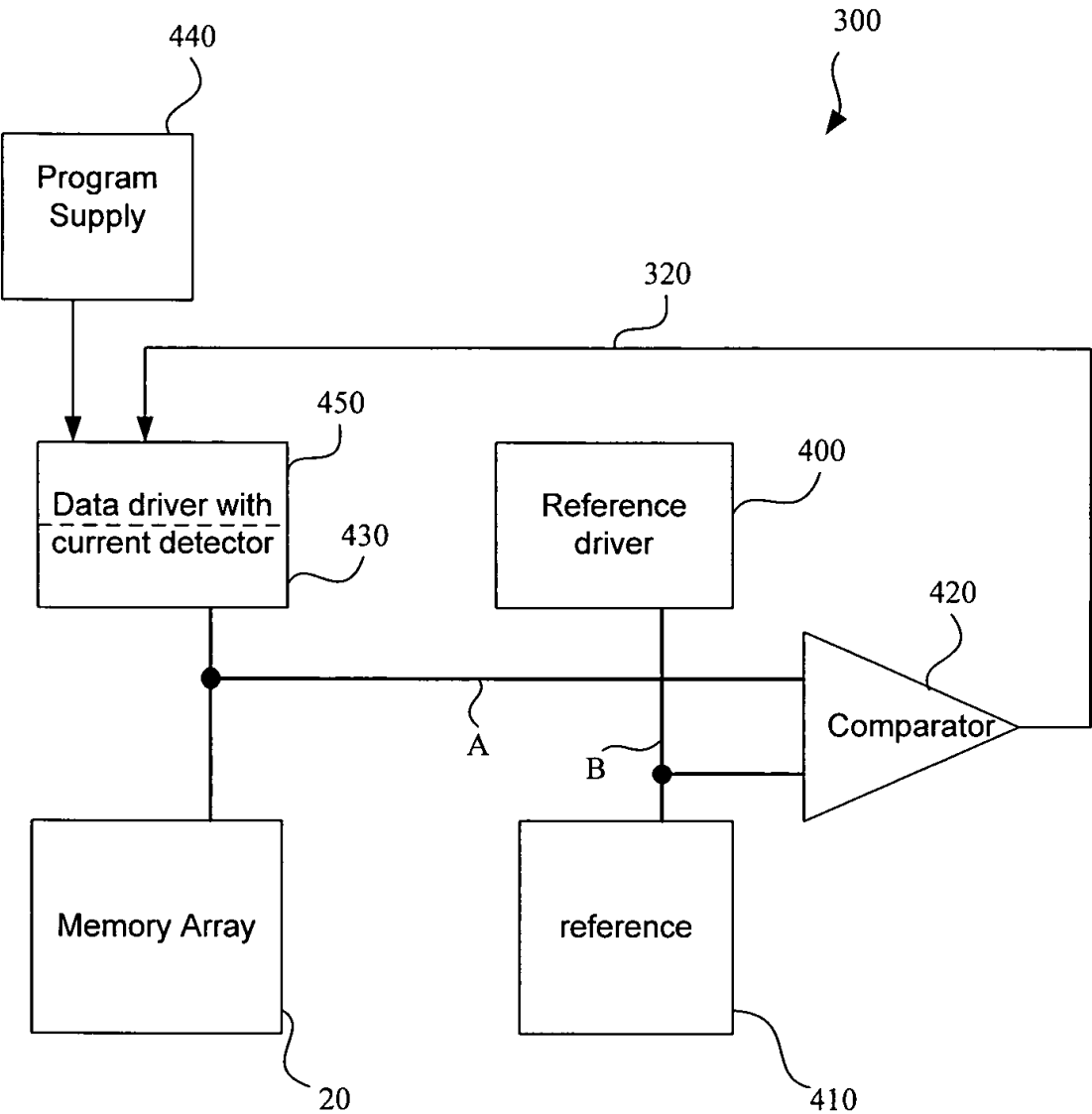


Figure 4A

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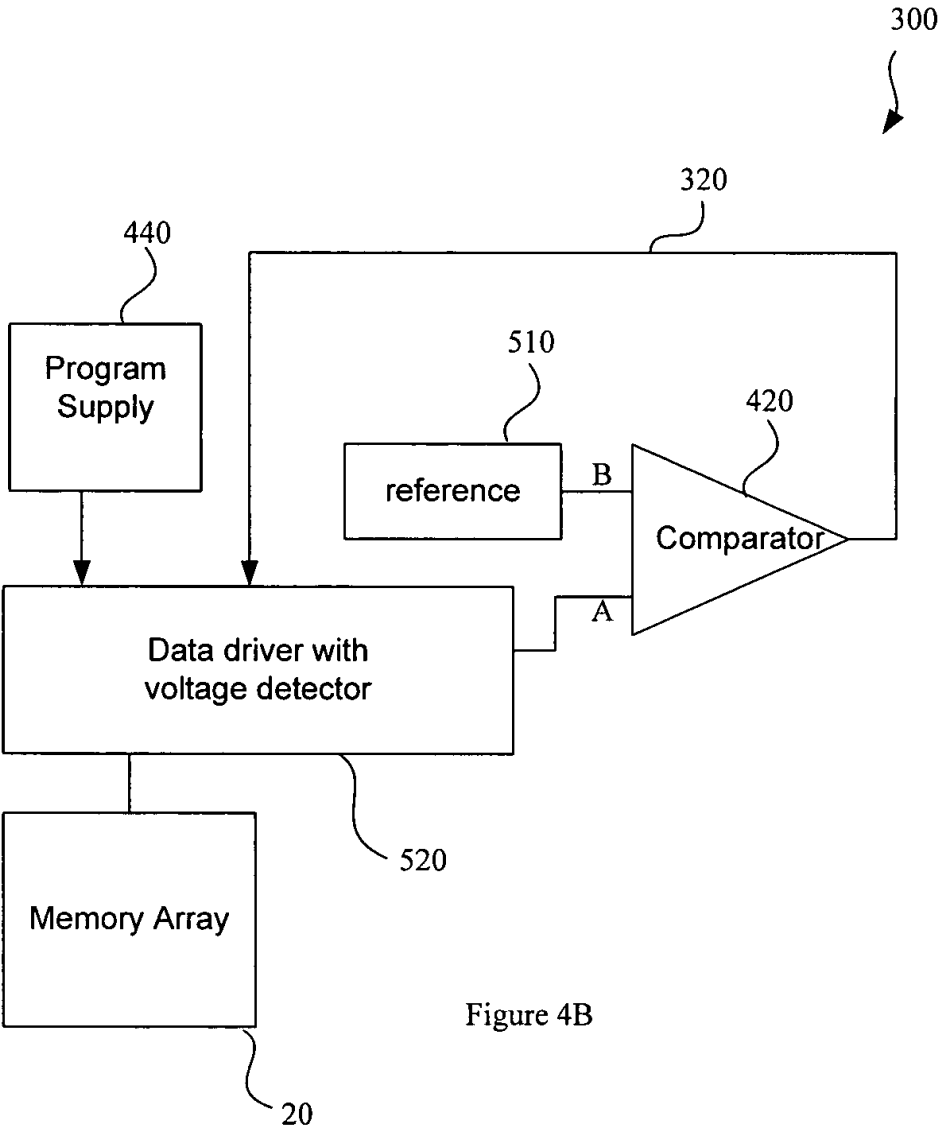


Figure 4B

An Adaptive Programming Technique for A Re-writable Resistive Memory Device

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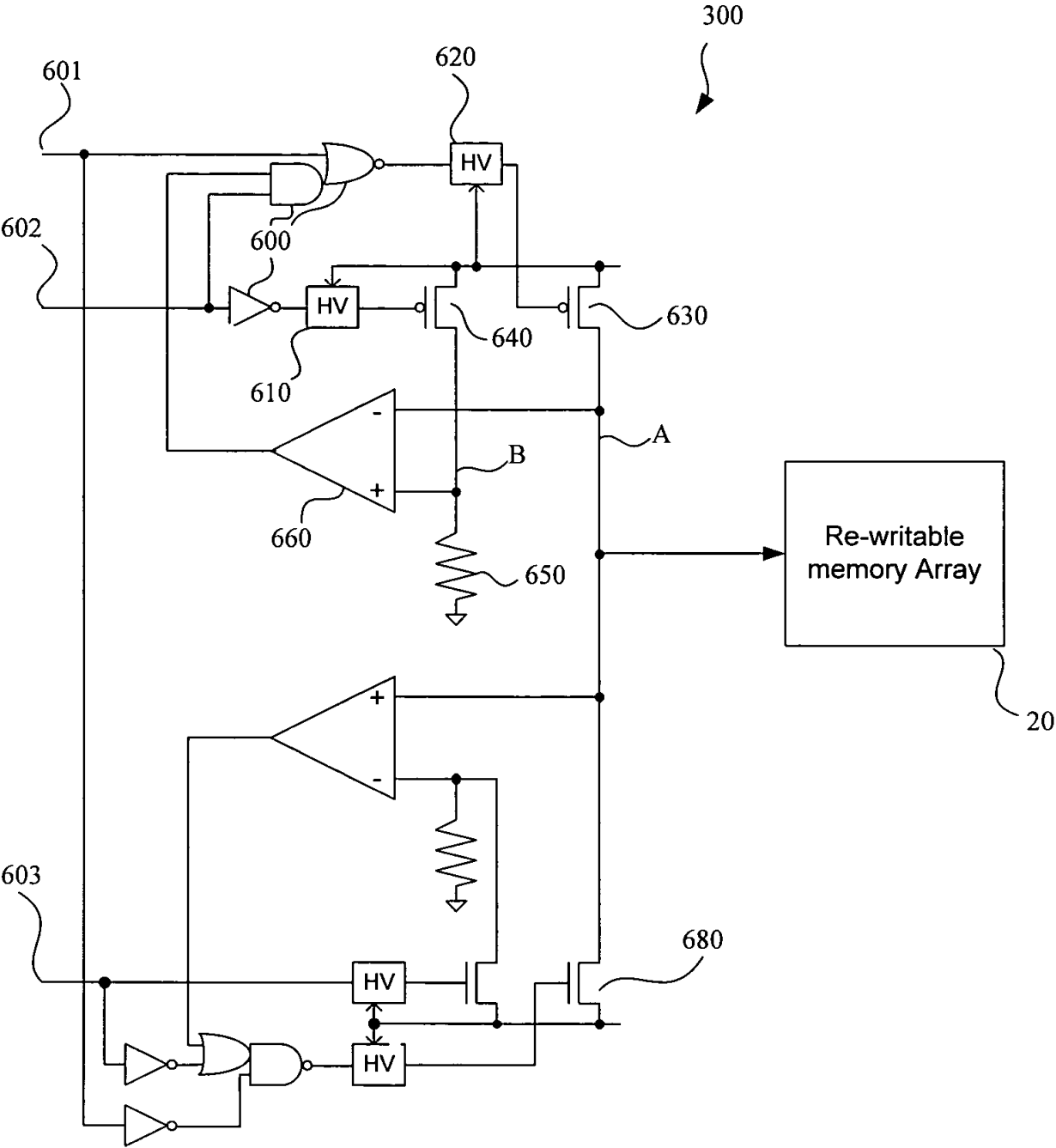


Figure 5

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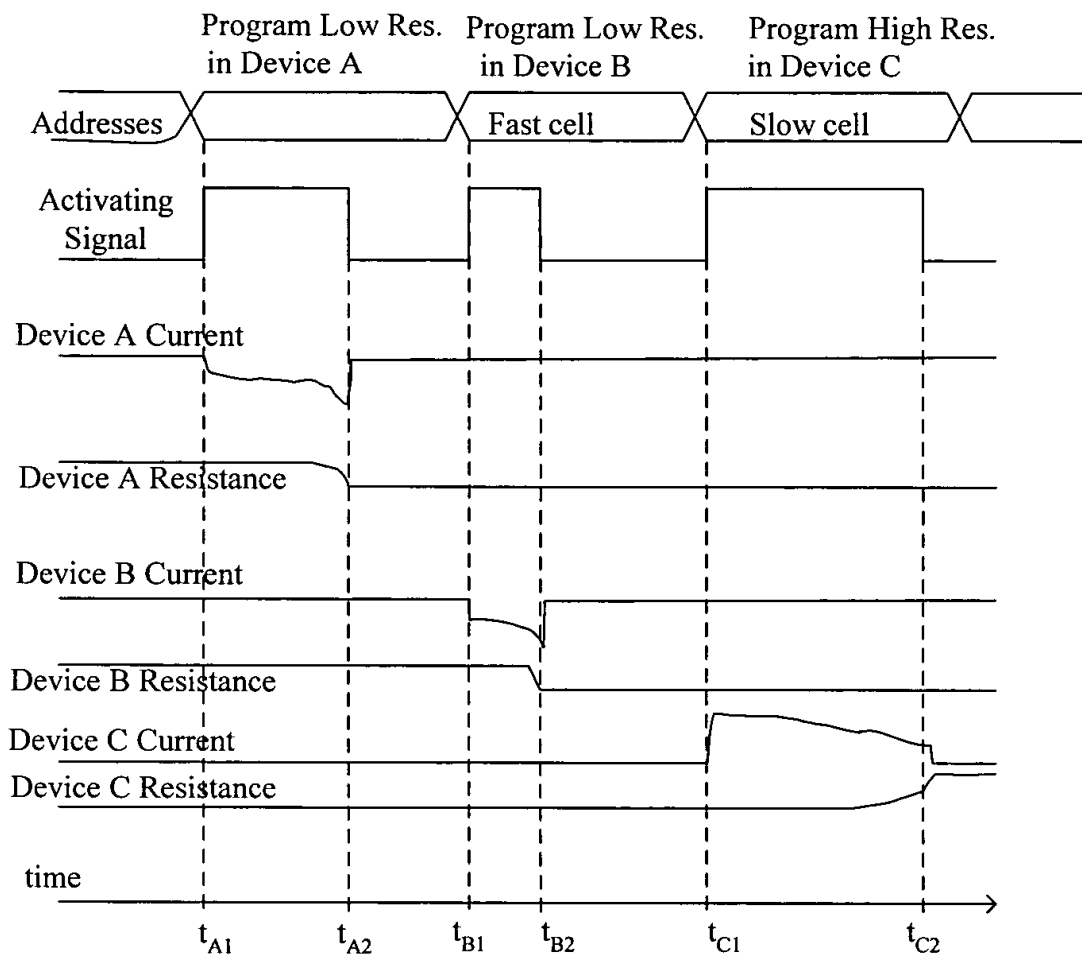


Figure 6

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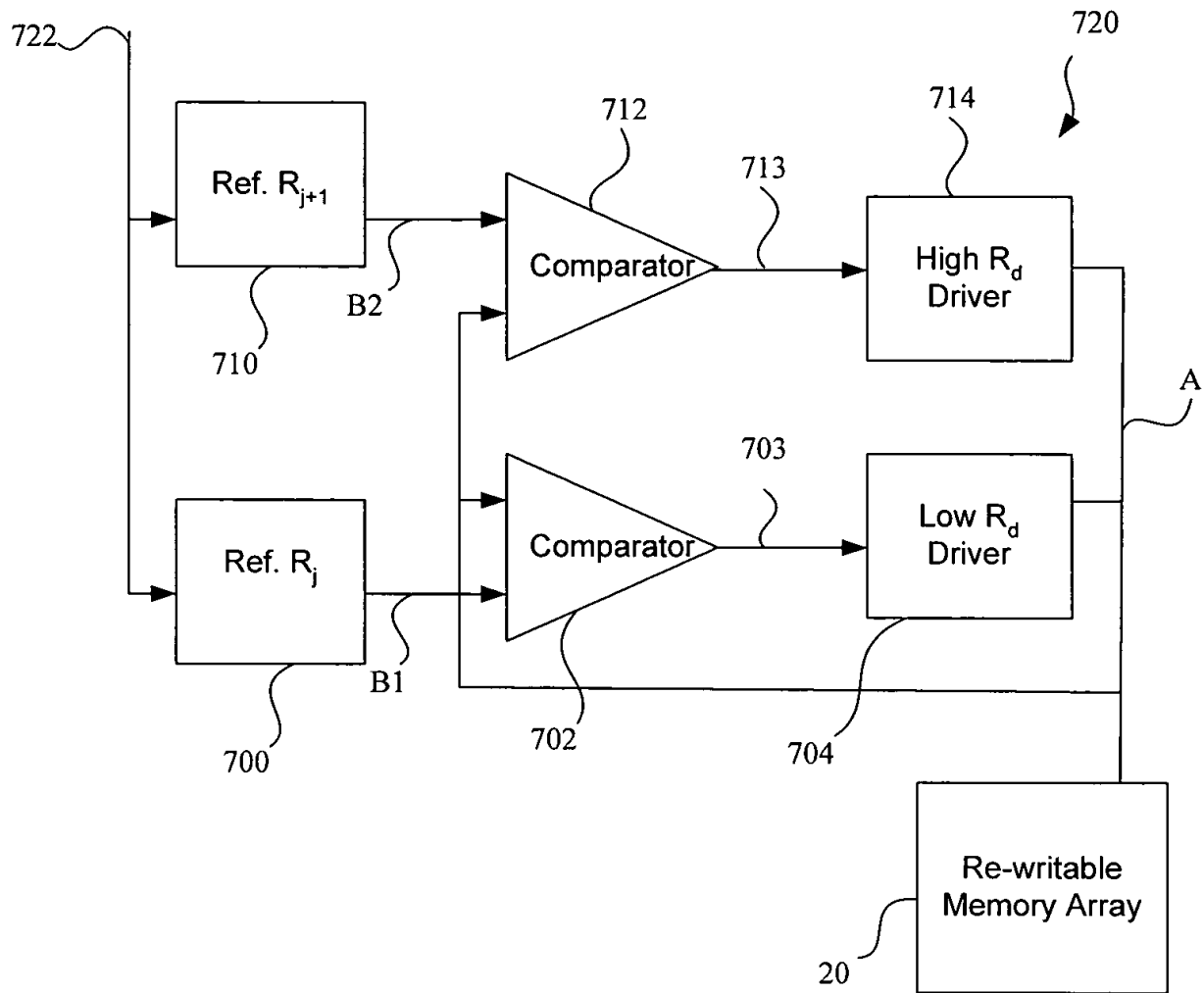


Figure 7

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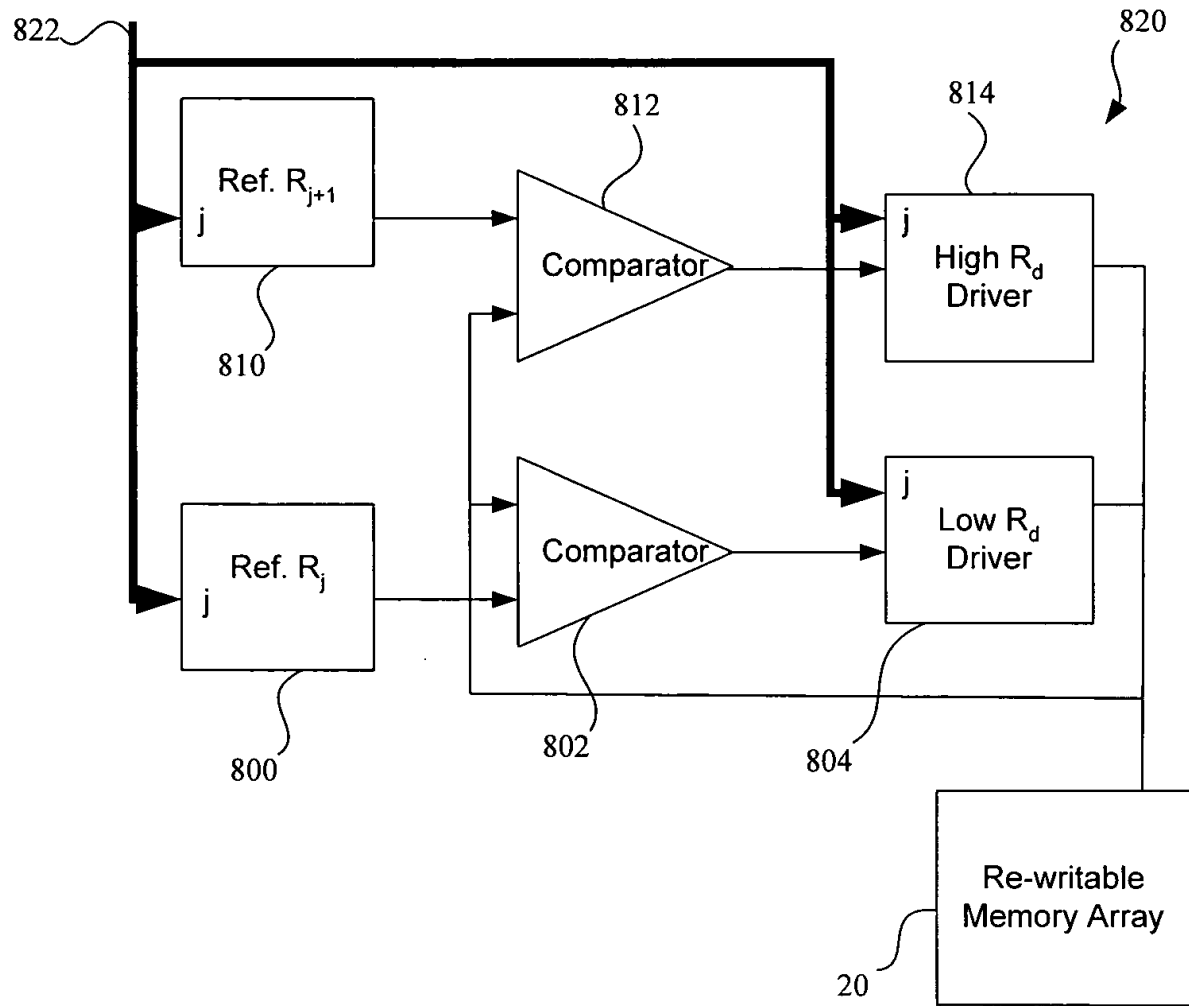


Figure 8